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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY ID	SHEETS DRAWING 6	TOTAL CLAIMS 41	INDEPENDENT CLAIMS 6
35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met Allowance				
Verified and Acknowledged Examiner's Signature _____ Initials _____				

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## TITLE

Method and apparatus for compensating duty cycle distortion in a data output signal from a memory device by delaying and distorting a reference clock

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